

"Laurea Magistrale in Electronic Engineering curriculum IES A.A. 2026/2027"

I semestre		II semestre		
	Insegnamento	CFU	Insegnamento	CFU
I anno	Electron Devices and Components	6	Optoelectronic Instrumentation and Sensors	9
	Hardware and Software Co-Design (modulo Digital Core)	6	Microwave and Photonic Components	6
	Hardware and Software Co-Design (modulo Analog-Digital Interfaces)	6	Analog and Mixed Signal Circuit Design	9
	Model-Based Digital Controls	6	Networking and Internet of Things	9
II anno	Emerging Nanoelectronics	9		
	Learning Based Signal Processing	6		
	Quantum and Integrated Photonics	6		
	Computer Aided Design of Electronic Systems	6		

Trainship/Design Activity/Eligible	21
Prova Finale/Tesi	15

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I semestre			II semestre	
	Insegnamento	CFU	Insegnamento	CFU
I anno	Electron Devices and Components	6	High Performance Electric Drives	6
	Hardware and Software Co-Design (modulo Digital Core)	6	Industrial Measurements	9
	Hardware and Software Co-Design (modulo Analog-Digital Interfaces)	6	Power Electronics	9
	Model-Based Digital Controls	6	Design and Modelling of Electronic Systems (Mechanical Design module)	3
			Design and Modelling of Electronic Systems (CFD Modelling)	3
II anno	Wide Bandgap Semiconductor Power Devices	6		
	Electromechanical Energy Storage and Conversion	9		
	Electromagnetic Compability and Signal Integrity	9		
	Modeling and Control of Electromechanical Systems	6		

Trainship/Design Activity/Eligible	21
Prova Finale/Tesi	15